

# JESHTA M RAO

Cybersecurity Professional | GRC & SOC Entry Level

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## PROFESSIONAL SUMMARY

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**ISO/IEC 27001 Lead Auditor** with 2 years of enterprise security experience at Bosch Global Software Technologies, seeking a GRC Analyst or SOC Analyst role in Australia. Hands-on background in security monitoring, vulnerability management, access control, and incident response within a global engineering environment supporting Honda Japan. Currently completing a Master of Cyber Security at RMIT University. Grounded in NIST CSF, ISO/IEC 27001, and risk management practices, with a demonstrated commitment to bridging technical security execution with governance and compliance objectives.

## KEY SKILLS

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**GRC & Frameworks:** ISO/IEC 27001 Lead Auditor, NIST CSF, Essential Eight (ACSC), Risk Assessment, Compliance Management, Policy Development, Control Assurance, Security Governance

**Risk & Privacy:** Risk Registers, Risk Treatment Planning, Third-Party Risk, Australian Privacy Act (APP), Vendor Assessment, Audit Support, Evidence Collection

**Security Operations:** Security Monitoring, Incident Response, Vulnerability Management, Access Control, Identity Management, SIEM, Security Event Handling

**Tools & Reporting:** ServiceNow GRC (familiar), Wireshark, Ghidra, CANalyzer, CANoe, Trace32, DOORS, Microsoft Office

## PROFESSIONAL EXPERIENCE

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### Bosch Global Software Technologies

August 2023 – December 2024

Senior Engineer, Cyber Security | Bengaluru, India

- Managed enterprise security monitoring and access controls for Honda Japan, overseeing security event triage and coordinating timely incident investigation and resolution across cross-functional teams.
- Coordinated vulnerability remediation activities with technical and business stakeholders, improving patch compliance and reducing organisational risk exposure.
- Contributed to incident response workflows, supporting security operations in alignment with established response procedures and SLA requirements.
- Implemented Secure Boot for automotive Video ECUs using HSM-based signature verification, ensuring firmware authenticity and integrity.
- Conducted security testing and validation of in-vehicle communication protocols (SecOC over CAN/Ethernet), validating controls against defined security requirements.
- Applied AUTOSAR security architecture principles to align embedded technical controls with organisational security requirements.

### Bosch Global Software Technologies

February 2023 – July 2023

Cyber Security Intern | Bengaluru, India

- Contributed to implementation of secure monitoring and logging controls aligned with organisational security baseline requirements.
- Performed security debugging using Trace32 for real-time memory inspection, peripheral monitoring, and breakpoint analysis.
- Collaborated with domain experts on proof-of-concept security implementations for automotive embedded systems.

## EDUCATION

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### Master of Cyber Security

February 2025 – December 2026 (Expected)

RMIT University, Melbourne, VIC, Australia | GPA: 3.3 / 4.0

### Bachelor of Engineering, Computer Science and Engineering

June 2019 – November 2022

JSS Science and Technology University, Mysore, India | GPA: 8.91 / 10

## PROJECTS & PROFESSIONAL INITIATIVES

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## GRC Drift Lab

October 2025 – Present

*Original framework + interactive web application | Solo personal initiative*

- Designed the **GRC Drift Model**, an original six-stage lifecycle framework that maps how compliance and security reality drift apart over time, helping GRC professionals spot the patterns before they surface as breaches.
- Built and deployed a public, interactive web application featuring an animated dual-cycle diagram (documented ideal vs silent reality) and click-through stage explainers.
- Researched and mapped real-world breaches (Optus, Medibank, Latitude, Equifax, Wells Fargo) to specific drift stages, sourced from primary documents (OAIC reports, ACMA filings, US Senate investigations).
- **Stack:** Next.js, TypeScript, Tailwind CSS, custom SVG animation, Vercel deployment, GitHub continuous delivery.
- **Live:** [grc-drift-lab.vercel.app](https://grc-drift-lab.vercel.app) | Repository: [github.com/Jeshta0564/grc-drift-lab](https://github.com/Jeshta0564/grc-drift-lab) (MIT licensed)

## AMBED, Adaptive Multi-Layer Behavioural Exfiltration Detector

February 2026 – May 2026

*Group Research Project | Project Leader | RMIT University (COSC3130)*

- **Project Leader** on a 5-member group designing AMBED, an adaptive framework to detect data exfiltration by malicious insiders and malware through fused host and network telemetry.
- Architected a four-layer detection pipeline combining statistical, LSTM-sequence, and Graph Neural Network embeddings via a gated ensemble, with MAML for drift adaptation and personalised federated learning for cross-organisational privacy.
- Authored research questions, scope, risk register, milestone planning, and overall project coordination; benchmarked against five published baselines on CERT Insider Threat and CIC-IDS-2018 datasets.
- *Bridges technical detection with GRC considerations: insider threat policy, monitoring controls, and privacy trade-offs.*

## GRC Through the Lens

September 2024 – Present

*Independent publication | Personal initiative*

- Author and maintain an independent blog interpreting cybersecurity through a Governance, Risk and Compliance lens, writing on audits, incidents, frameworks, and the everyday decisions that shape security programs.
- Run a recurring control mapping series mapping ISO/IEC 27001 Annex A controls to real-world risks, alongside writeups for aspiring and practising GRC professionals.

## ISO/IEC 27001 & NIST Control Mapping Initiative

December 2025 – Present

*ServiceNow GRC hands-on project | Personal initiative*

- Building practical control mappings and gap analysis artefacts inside the ServiceNow GRC platform, configuring control libraries, authoring policy and control statements, and producing risk registers and control assurance summaries.
- Designed to demonstrate end-to-end GRC tooling fluency beyond theoretical framework knowledge.

## Secure Voting Platform

September 2025 – October 2025

*Secure Software Development Project | RMIT University | Group project*

- Designed a secure voter registration and voting workflow with emphasis on integrity, authentication, and role-based access control.
- Applied secure SDLC principles to align technical controls with trust, audit, and compliance objectives.

## VOLUNTEERING & INDUSTRY ENGAGEMENT

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### Session Host, CyberCon 2025 Melbourne

October 2025

*Australian Information Security Association (AISA)*

- Served as a conference session host at Australia's largest annual cybersecurity conference, coordinating and hosting five industry speakers across cybersecurity and governance tracks.

## CERTIFICATIONS & TRAINING

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- **ISO/IEC 27001 Lead Auditor** (Credly verified)
- **Cyber Security Governance, Risk and Compliance (GRC) Mastery** (Credly verified)
- Third-Party Risk Management 2025, SecurityScorecard Academy
- Cyber Security Risk Management, Udemy
- AUTOSAR Security Architecture, Udemy
- CAN Protocol Security, Udemy